

# $\tau$ -Equivalences for analysis of concurrent systems modelled by Petri nets with silent transitions\*

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The paper is devoted to the investigation of behavioural equivalences for Petri nets with silent transitions ( $\tau$ -equivalences). Basic  $\tau$ -equivalences and back-forth  $\tau$ -bisimulation equivalences are supplemented by new ones, giving rise to the complete set of equivalence notions in interleaving / true concurrency and linear / branching time semantics. Their interrelations are examined for the general Petri nets with silent transitions as well as for the subclass of sequential nets. In addition, the preservation of all the equivalence notions by refinements is investigated.

## 1. Introduction

The notion of equivalence is central in any theory of systems. It allows one to compare systems taking into account particular aspects of their behaviour.

Petri nets became a popular formal model for design of concurrent and distributed systems. Silent transitions are transitions labelled by a special *silent* action  $\tau$  which represents an internal activity of a system to be modelled and it is invisible for an external observer.

Equivalences abstracting of silent actions are called  $\tau$ -equivalences (these are labelled by the symbol ' $\tau$ ' to distinguish them of relations not abstracting of silent actions).

Behavioural equivalences can be classified depending on semantics of concurrency they impose. The following kinds of semantics are known.

- **Interleaving:** a concurrent happening of actions is interpreted as their occurrence in any possible order.
- **Step:** a concurrency of actions is a basic notion, but their causal dependencies are not respected.
- **Partial word:** causal dependencies of actions are respected in part via partially ordered multisets (pomsets) of actions, and a pomset may be modelled by a less sequential one (i.e. having less strict partial order).
- **Pomset:** causal dependencies of actions are fully respected, and pomsets of actions should coincide to model each other.

The following basic notions of  $\tau$ -equivalences are known.

- $\tau$ -trace equivalences (they respect only protocols of systems' behaviour): interleaving ( $\equiv_i^\tau$ ) [4], step ( $\equiv_s^\tau$ ) [4], partial word ( $\equiv_{pw}^\tau$ ) [7] and pomset ( $\equiv_{pom}^\tau$ ) [4].
- Usual  $\tau$ -bisimulation equivalences (they respect the branching structure of systems' behaviour): interleaving ( $\leftrightarrow_i^\tau$ ) [4], step ( $\leftrightarrow_s^\tau$ ) [4], partial word ( $\leftrightarrow_{pw}^\tau$ ) [7] and pomset ( $\leftrightarrow_{pom}^\tau$ ) [4].
- $ST$ - $\tau$ -bisimulation equivalences (they respect the duration or maximality of events in systems' behaviour): interleaving ( $\leftrightarrow_{iST}^\tau$ ) [7], partial word ( $\leftrightarrow_{pwST}^\tau$ ) [7] and pomset ( $\leftrightarrow_{pomST}^\tau$ ) [7].
- History preserving  $\tau$ -bisimulation equivalences (they respect the "past" or "history" of systems' behaviour): pomset ( $\leftrightarrow_{pomh}^\tau$ ) [4].

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- *History preserving ST- $\tau$ -bisimulation equivalences* (they respect the “history” and the duration or maximality of events in systems’ behaviour): pomset ( $\leftrightarrow_{pomhST}^{\tau}$ ) [4].
- *Usual branching  $\tau$ -bisimulation equivalences* (they respect the branching structure of systems’ behaviour taking a special care of silent actions): interleaving ( $\leftrightarrow_{ibr}^{\tau}$ ) [4].
- *History preserving branching  $\tau$ -bisimulation equivalences* (they respect the “history” and the branching structure of systems’ behaviour taking a special care of silent actions): pomset history preserving ( $\leftrightarrow_{pomhbr}^{\tau}$ ) [4].
- *Isomorphism ( $\simeq$ )* (i.e. coincidence of systems up to renaming of their components).

Another important group of equivalences are back-forth bisimulation ones based on the idea that a bisimulation relation does not only require systems to simulate behaviour of each other in the forward direction but also when going back in history. They are closely connected with equivalences of logics with past modalities. In [5], the set of all possible back-forth equivalence notions was proposed in interleaving, step, partial word and pomset semantics for event structures without silent actions. The new notion of  $\tau$ -equivalence was proposed for event structures with silent actions: pomset back pomset forth ( $\leftrightarrow_{pombpomf}^{\tau}$ )  $\tau$ -bisimulation equivalence. Its coincidence with  $\leftrightarrow_{pomhbr}^{\tau}$  was proved.

Working in the framework of Petri nets with silent transitions, in this paper we continue the research of [6] and extend the set of basic notions of  $\tau$ -equivalences by interleaving ST-branching  $\tau$ -bisimulation ( $\leftrightarrow_{iSTbr}^{\tau}$ ) and pomset history preserving ST-branching  $\tau$ -bisimulation ( $\leftrightarrow_{pomhSTbr}^{\tau}$ ). Thus, we combine a branching idea (taking a special care of silent actions involved in conflicts) and ST-idea (treating occurrences of visible actions as having a beginning and an end). We also define a multi-event structure ( $\equiv_{mes}^{\tau}$ ) equivalence to respect all conflicts of actions. We complete back-forth  $\tau$ -equivalences from [5] by 6 new notions in interleaving – pomset semantics: interleaving back step forth ( $\leftrightarrow_{ibsf}^{\tau}$ ), interleaving back partial word forth ( $\leftrightarrow_{ibpwf}^{\tau}$ ), interleaving back pomset forth ( $\leftrightarrow_{ibpomf}^{\tau}$ ), step back step forth ( $\leftrightarrow_{sbsf}^{\tau}$ ), step back partial word forth ( $\leftrightarrow_{sbpwf}^{\tau}$ ) and step back pomset forth ( $\leftrightarrow_{sbpomf}^{\tau}$ ) bisimulation equivalences. We compare all the  $\tau$ -equivalences and obtain a diagram of their interrelations.

In [2], the SM-refinement operator for Petri nets was proposed which “replaces” their transitions by SM-nets, a special subclass of the state machine nets. We check all the  $\tau$ -equivalences for preservation by SM-refinements to find out which of them may be used in top-down design. We show that  $\leftrightarrow_{iSTbr}^{\tau}$ ,  $\leftrightarrow_{pomhSTbr}^{\tau}$  and  $\equiv_{mes}^{\tau}$ , i.e., all the new basic equivalences considered in this paper are preserved by SM-refinements. Thus, we have branching and conflict preserving equivalences which may be used for multilevel design. In the literature stability w.r.t. SM-refinements was proved only for  $\leftrightarrow_{pomhST}^{\tau}$ , see [2], and for  $\leftrightarrow_{iST}^{\tau}$ , see [3]. For other ST- $\tau$ -bisimulation equivalences, preservation was proved in [7], but it was done on event structures and another refinement operator was used. The preservation of trace  $\tau$ -equivalences was not established before. Thus, our results for  $\leftrightarrow_{pwST}^{\tau}$ ,  $\leftrightarrow_{pomST}^{\tau}$ ,  $\equiv_{pw}^{\tau}$  and  $\equiv_{pom}^{\tau}$  are also new.

In addition, we investigate the interrelations of all the  $\tau$ -equivalence notions on sequential nets to understand the impact of concurrency on equivalence notions. In the literature, only the coincidence of  $\leftrightarrow_{i}^{\tau}$  and  $\leftrightarrow_{pomh}^{\tau}$  was proved on sequential nets, see [2]. We extend these results to all the mentioned equivalence relations.

Due to the space restrictions, we do not present any definitions here and concentrate more on the presentation of the main results obtained. Some basic notions can be found in [6].

## 2. Interrelations between the $\tau$ -equivalences

Let us consider interrelations between all the mentioned  $\tau$ -equivalences.

**Proposition 1.** *Let  $\star \in \{i, s, pw, pom\}$ . For nets  $N$  and  $N'$  we have:*

1.  $N \leftrightarrow_{pwb\star f}^{\tau} N' \Leftrightarrow N \leftrightarrow_{pomb\star f}^{\tau} N'$ ;
2.  $N \leftrightarrow_{\star bif}^{\tau} N' \Leftrightarrow N \leftrightarrow_{\star b\star f}^{\tau} N'$ ;

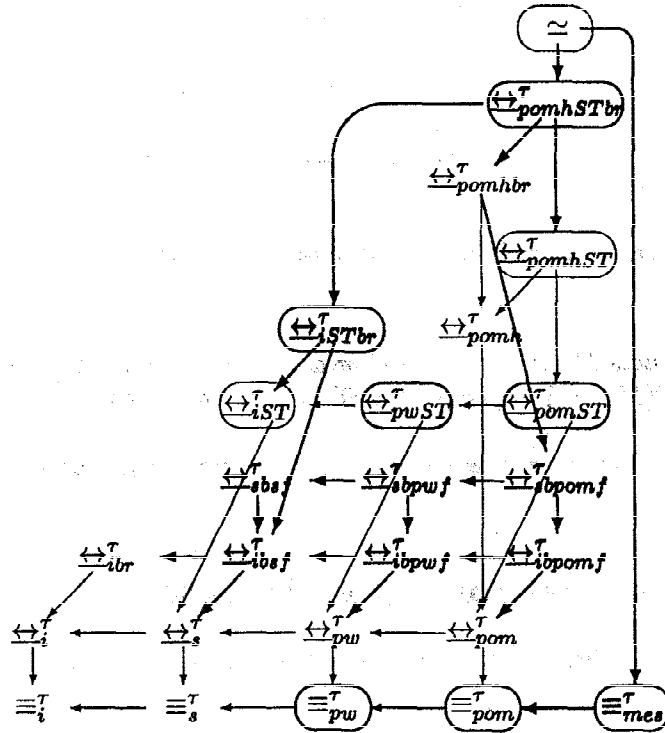


Figure 1. Interrelations between the  $\tau$ -equivalences and their preservation by SM-refinements

3.  $N \leftrightarrow_{ibr}^{\tau} N' \Leftrightarrow N \leftrightarrow_{ibr}^{\tau} N'$  [5];
4.  $N \leftrightarrow_{pombpom,f}^{\tau} N' \Leftrightarrow N \leftrightarrow_{pomhbr}^{\tau} N'$  [5];
5.  $N \leftrightarrow_{iSTbr}^{\tau} N' \Rightarrow N \leftrightarrow_{ibsf}^{\tau} N'$ .

In the following, the symbol ‘.’ will denote an empty alternative. The symbols of equivalences subscribed by it are considered as having no subscriptions.

**Theorem 1.** Let  $\leftrightarrow, \Leftarrow \in \{\equiv^{\tau}, \leftrightarrow^{\tau}, \simeq\}$  and  $\star, \star\star \in \{-, i, s, pw, pom, iST, pwST, pomST, pomh, pomhST, ibr, pomhbr, iSTbr, pomhSTbr, mes, ibsf, ibpwf, ibpomf, sbsf, sbpwf, sbpomf\}$ . For nets  $N$  and  $N'$  we have:  $N \leftrightarrow_{\star} N' \Rightarrow N \Leftarrow_{\star\star} N'$  iff there exists a directed path from  $\leftrightarrow_{\star}$  to  $\Leftarrow_{\star\star}$  in the graph in Figure 1.

In Figure 1, the new equivalence notions introduced in this paper are printed in bold font, and the newly established interrelations between the equivalences are depicted by thick lines.

### 3. Transition refinement

In this section we treat the considered  $\tau$ -equivalences for preservation by SM-transition refinements [2].

A refinement operator  $ref(N, a, D)$  “replaces” all transitions of a net  $N$  labelled by an action  $a$  by SM-net  $D$ . SM-nets are a special subclass of state machine nets. An equivalence is *preserved by refinements*, if equivalent nets remain equivalent after applying any refinement operator to them.

**Theorem 2.** Let  $\leftrightarrow \in \{\equiv^{\tau}, \leftrightarrow^{\tau}, \simeq\}$  and  $\star \in \{-, i, s, pw, pom, iST, pwST, pomST, pomh, pomhST, ibr, iSTbr, pomhSTbr, pomhbr, mes, ibsf, ibpwf, ibpomf, sbsf,$

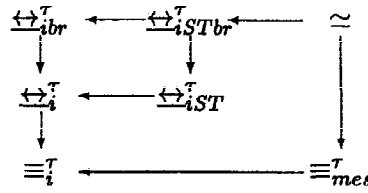


Figure 2. Interrelations between the  $\tau$ -equivalences on sequential nets

$sbpwf, sbpomf\}$ . For nets  $N, N'$  and an action  $a$  which labels transitions of these nets and SM-net  $D: N \leftrightarrow_* N' \Rightarrow ref(N, a, D) \leftrightarrow_* ref(N', a, D)$  iff the equivalence  $\leftrightarrow_*$  is in oval in Figure 1.

In Figure 1, thick ovals correspond to the new results on preservation by SM-refinements here obtained.

#### 4. The $\tau$ -equivalences on sequential nets

Let us consider the  $\tau$ -equivalences on sequential nets, where no two transitions can be fired concurrently in any reachable marking.

**Proposition 2.** For sequential nets  $N$  and  $N'$  we have:

1.  $N \equiv_i^{\tau} N' \Leftrightarrow N \equiv_{pom}^{\tau} N'$ ;
2.  $N \leftrightarrow_i^{\tau} N' \Leftrightarrow N \leftrightarrow_{pomh}^{\tau} N'$  [2];
3.  $N \leftrightarrow_{iST}^{\tau} N' \Leftrightarrow N \leftrightarrow_{pomhST}^{\tau} N'$ ;
4.  $N \leftrightarrow_{ibr}^{\tau} N' \Leftrightarrow N \leftrightarrow_{pomhbr}^{\tau} N'$ ;
5.  $N \leftrightarrow_{iSTbr}^{\tau} N' \Leftrightarrow N \leftrightarrow_{pomhSTbr}^{\tau} N'$ .

**Theorem 3.** Let  $\leftrightarrow, \Leftarrow \in \{\equiv^{\tau}, \leftrightarrow^{\tau}, \simeq\}$ ,  $\star, \star\star \in \{-, i, iST, ibr, iSTbr, mes\}$ . For sequential nets  $N$  and  $N'$  we have:  $N \leftrightarrow_{\star} N' \Rightarrow N \Leftarrow_{\star\star} N'$  iff there exists a directed path from  $\leftrightarrow_{\star}$  to  $\Leftarrow_{\star\star}$  in the graph in Figure 2.

#### 5. Conclusion

In this paper, a group of basic  $\tau$ -equivalences and back-forth  $\tau$ -bisimulation equivalences has been examined and supplemented with new ones. We have also compared them on the whole class of Petri nets as well as on a subclass of sequential nets. All the considered  $\tau$ -equivalences have been checked for preservation by SM-refinements.

Further research may consist in the investigation of  $\tau$ -variants of place bisimulation equivalences [1] which are used for effective semantically correct reduction of nets. In this paper a notion of interleaving place  $\tau$ -bisimulation equivalence has been proposed, and its usefulness for simplification of concurrent systems has been demonstrated.

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